

Vivace Flow Blood Flow Speed Measurement Probe

ENSC 440

Spring 2011

Agenda

Introduction

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget



Introduction High Level System Design

Introduction

Calibration and Testing

Future Work

Timeline and Budget

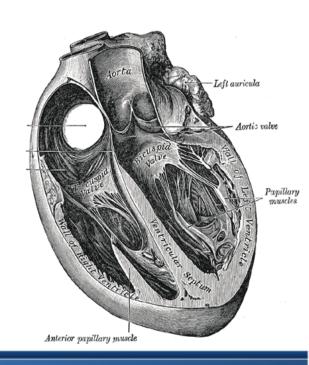
Conclusion

Purpose

 To Measure Instantaneous Blood Flow Speed

Kardium

 Designs solutions for cardiovascular diseases



System Overview

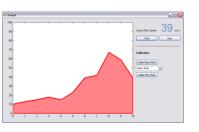
High Level System Design

Calibration and Testing

Future Work

Timeline and Budget

Conclusion







*Unisca*Suite™

Allows user to configure common speed measurement

Displays flow speed information to the user in relogs all information a log file

*Genio*BoxTM

Tells probe to s collecting da

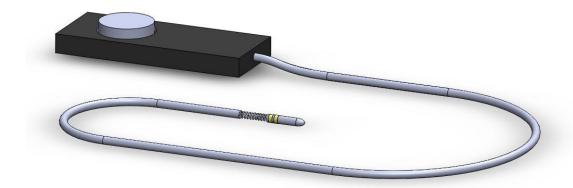
Processes data received from probe and send the UI *Piccolo* Probe[™]

Collects flow speed information

Sends the information to the GeniusBox

Introduction High Level System Design Calibration and Testing **Future Work** Timeline and Budget Conclusion

PiccoloProbe (Mechanical)



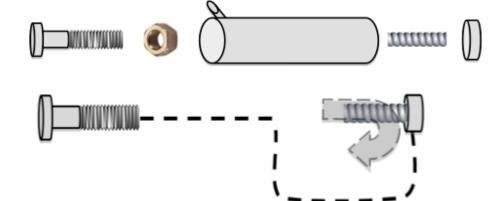


Introduction High Level System Design Calibration and Testing **Future Work** Timeline and Budget

PiccoloProbe (Mechanical)

PiccoloProbe's
Overall
Mechanical
Components
Steering

Summary





PiccoloProbe (Mechanical)

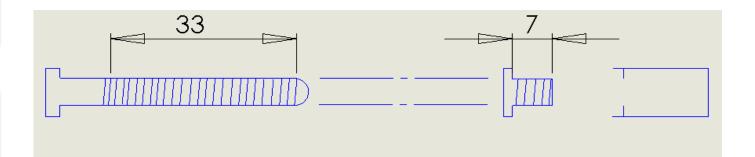
High Level System Design

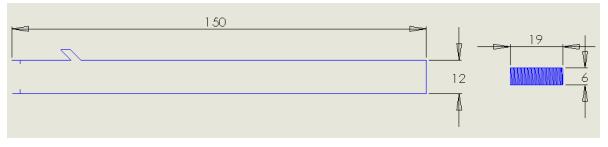
Calibration and Testing

Future Work

Timeline and Budget

Conclusion





Units: mm

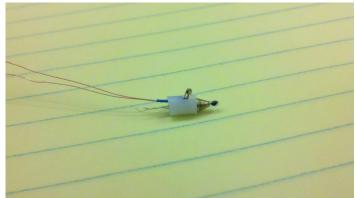


PiccoloProbe (Mechanical)

High Level System Design

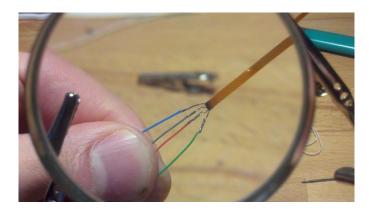
Calibration and Testing





Future Work

Timeline and Budget





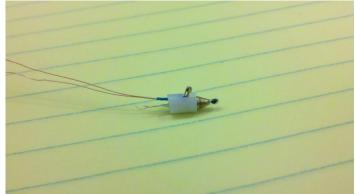


PiccoloProbe (Mechanical)

High Level System Design

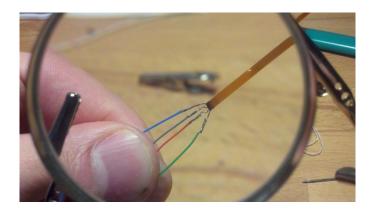
Calibration and Testing

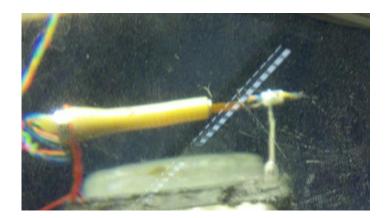




Future Work

Timeline and Budget







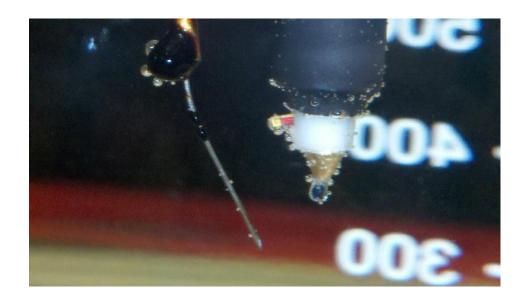
PiccoloProbe (Mechanical)

High Level System Design

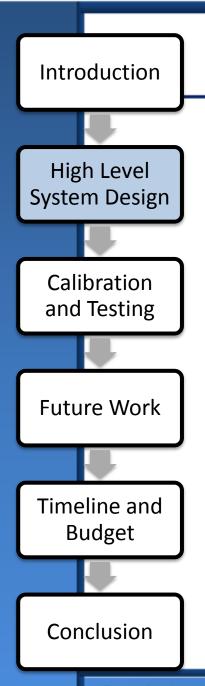
Calibration and Testing

Future Work

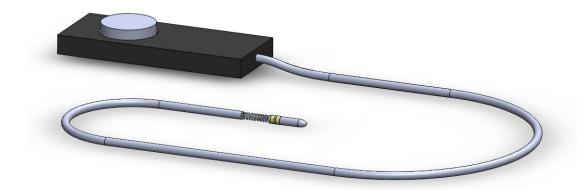
Timeline and Budget







PiccoloProbe (Electronic)





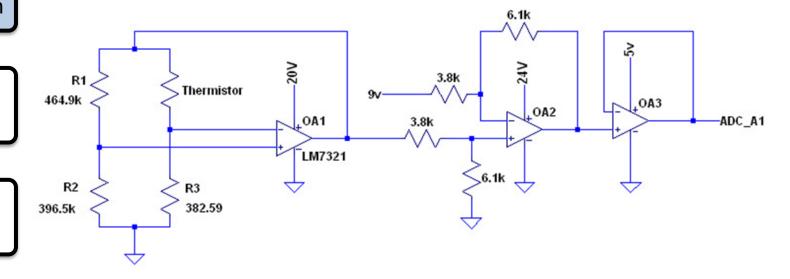
PiccoloProbe (Electronic)

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget





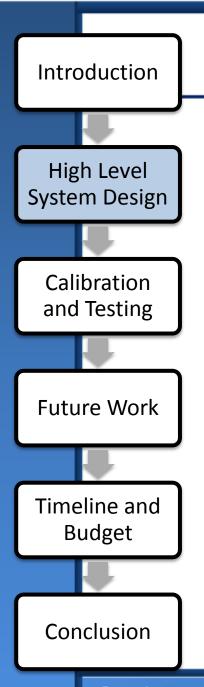
Introduction **High Level** System Design Calibration and Testing **Future Work**

PiccoloProbe (Electronic)

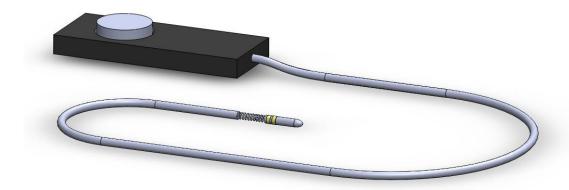
Characteristics of the Flow Sensing Thermistor

- Honeywell Glass bead NTC thermistor
- Low dissipation constant (0.1mW/°C)
- Time constant in air: 0.5s
- Small size: 1.14 mm diameter
- Relatively large β value (relation ship between resistance and temperature)
- Drawback: ± 20% tolerance which made calibration of thermistor vital

Timeline and Budget



GenioBox



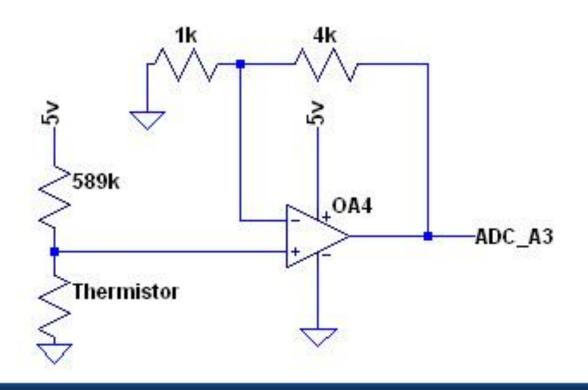


Introduction **High Level** System Design Calibration and Testing **Future Work** Timeline and Budget

Conclusion

GenioBox

- Epcos glass encapsulated temperature measuring thermistor
- 0.4mW/°C dissipation constant





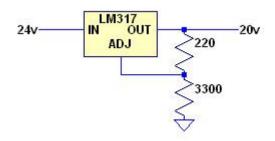
GenioBox

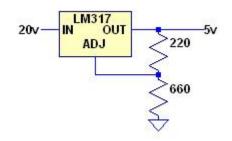
High Level System Design

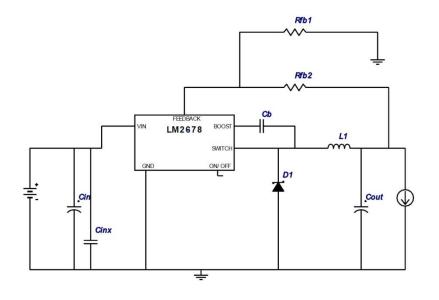
Calibration and Testing

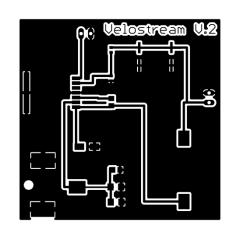
Future Work

Timeline and Budget











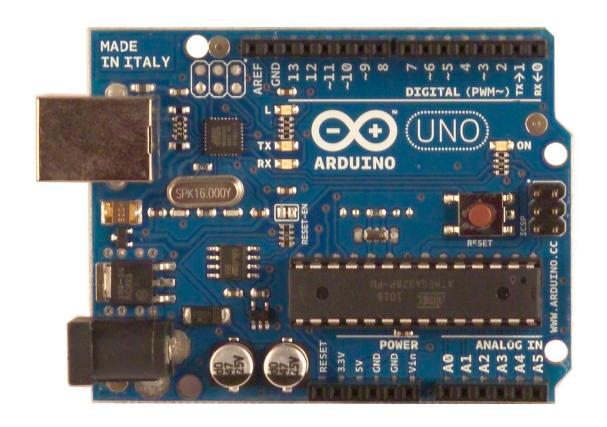
GenioBox

High Level System Design

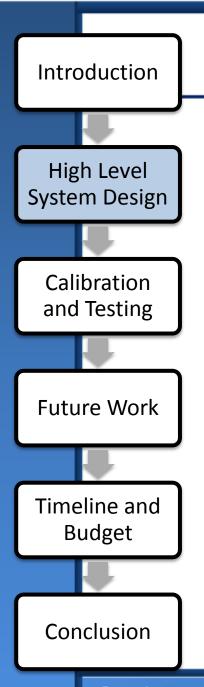
Calibration and Testing

Future Work

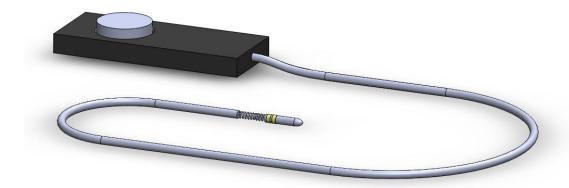
Timeline and Budget







UniscaSuite





Introduction **High Level** System Design Calibration and Testing **Future Work** Timeline and Budget

Conclusion

UniscaSuite

- Uses C# and the .NET Framework
- Uses Windows Forms to create the User interface
- Uses the .NET IO suite for COM port and logging



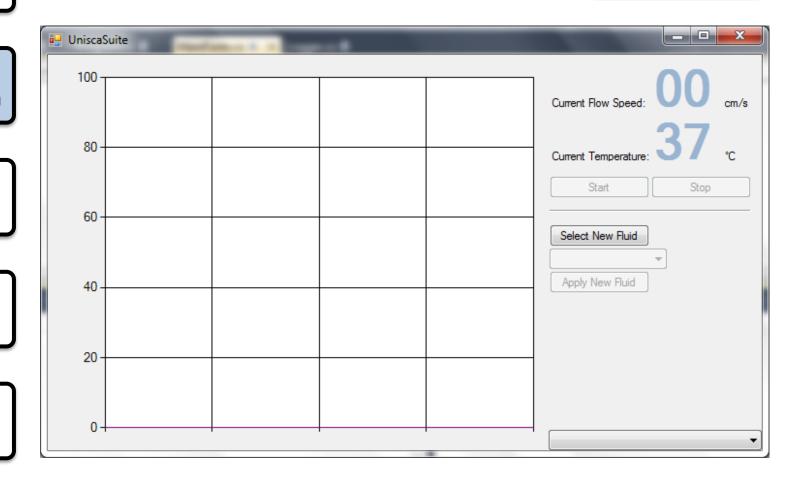
UniscaSuite

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget





UniscaSuite

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget

```
15_Apr_2011-1806 - Notepad
File Edit Format View Help
Time:,Speed:,Voltage:,Resistance
18:06:16.379,132.613244744781,4.9022,0.4731
18:06:16.394,56.238000939968,3.9998,0.4488
18:06:16.426,37.246340468484,3.6340,0.4341
18:06:16.426,27.6993005751563,3.3950,0.4634
18:06:16.426,23.1224923950327,3.2584,0.4634
18:06:16.426,19.7961567660553,3.1462,0.4292
18:06:16.441,17.6956417705796,3.0681,0.4390
18:06:16.457,15.3055606302276,2.9706,0.4195
18:06:16.457,14.6353901521859,2.9413,0.4195
18:06:16.472,13.5665377461035,2.8925,0.4390
18:06:16.488,12.4586169358843,2.8389,0.3805
18:06:16.504,11.8812994009842,2.8096,0.4585
18:06:16.519,11.7866634242264,2.8047,0.4780
18:06:16.519,10.875273781824,2.7560,0.4146
18:06:16.535,10.6981798704041,2.7462,0.4292
18:06:16.535,10.6981798704041,2.7462,0.4390
18:06:16.550,10.4381932200197,2.7316,0.4000
18:06:16.566,10.0138445588414,2.7072,0.4049
18:06:16.582,10.0980518769237,2.7121,0.4292
18:06:16.597,9.84693969315622,2.6974,0.4439
18:06:16.597,9.20239696581028,2.6584,0.3805
18:06:16.613,9.68370779253127,2.6877,0.5219
```



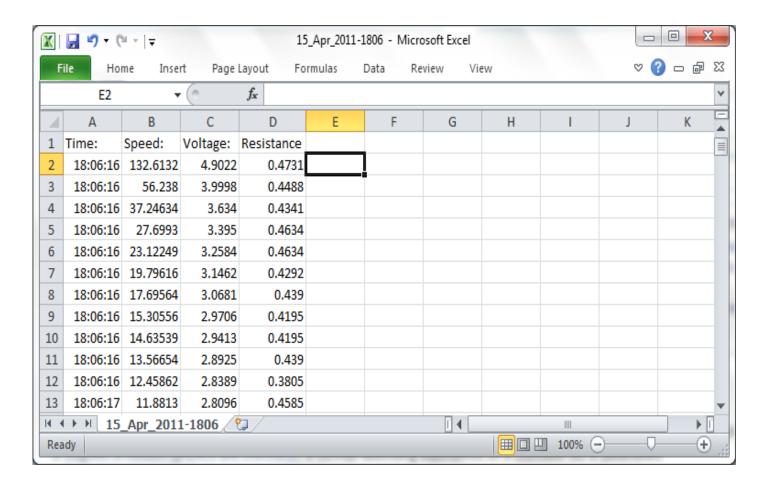
UniscaSuite

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget





Introduction High Level System Design Calibration and Testing **Future Work** Timeline and Budget Conclusion

UniscaSuite

- Used Nullsoft Scriptable Installer
 System (NSIS) to create installer
- Uses scripts to generate installers
- Installs *Unisca*Suite and .NET if needed
- Includes uninstaller



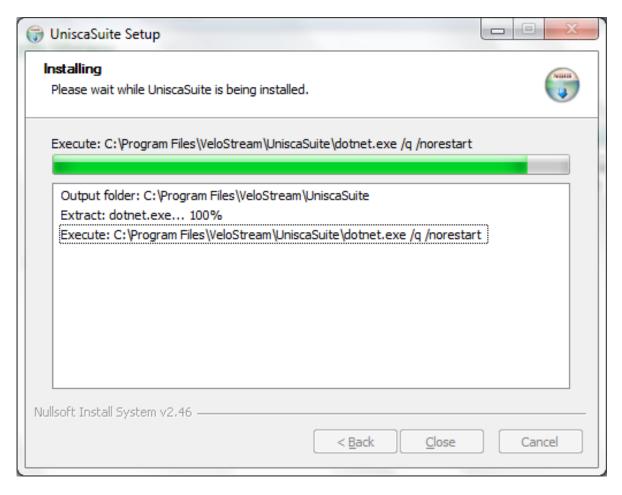
UniscaSuite

High Level System Design

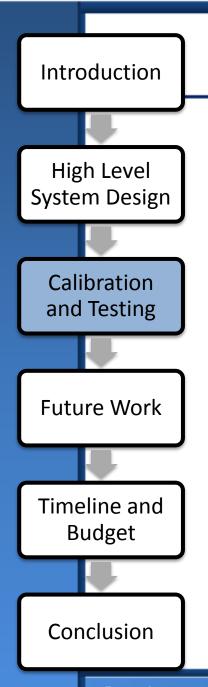
Calibration and Testing

Future Work

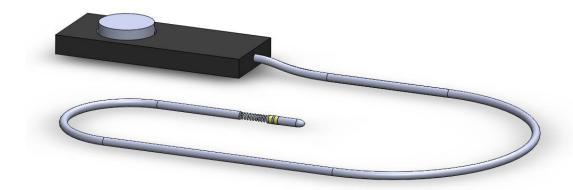
Timeline and Budget







Calibration and Testing





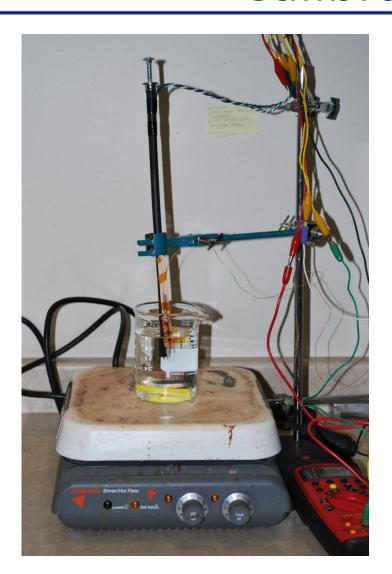
Calibration

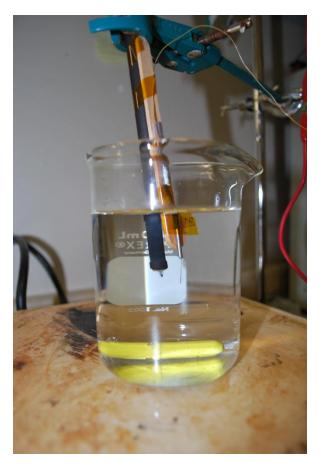
High Level System Design

Calibration and Testing

Future Work

Timeline and Budget







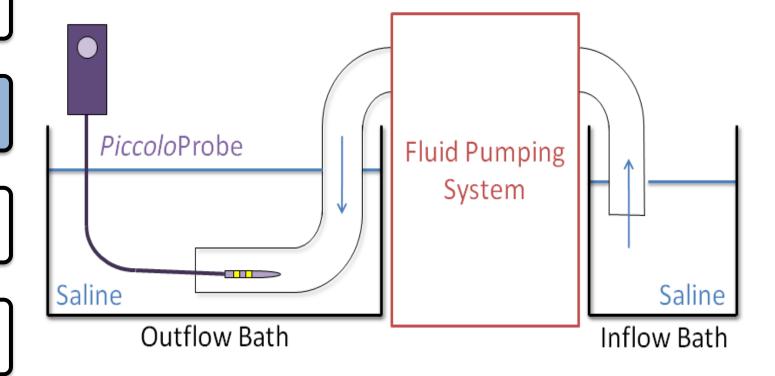
Calibration and Testing

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget





Calibration and Testing

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget





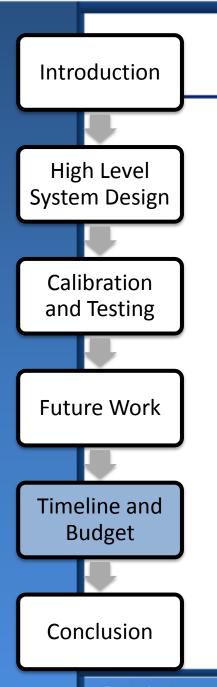
Introduction High Level System Design Calibration and Testing **Future Work** Timeline and Budget Conclusion

Future Work

Improvements:

- Use of DAC card instead of microcontroller to improve accuracy and reduce noise
- Optimize components (i.e., better op-amps, more flexible tip of probe)





Timeline and Budget



Timeline and Budget

High Level System Design

Calibration and Testing

Future Work

Timeline and Budget

Item		Budgeted Cost	Required Cost
Electronics Components	Microcontroller, Power Supplies, PCB Components	\$200.00	\$289.35
	Analog Circuitry and Sensors	\$250.00	\$359.01
Mechanical Components	Probe Materials	\$150.00	\$52.00
Testing Device	Flow Machine, Flowmeter	\$200.00	\$0.00
Shipping		\$100.00	\$38.49
Contingency		\$100.00	\$0.00
Sum:		\$1000.00	\$737.86



Timeline and Budget

High Level System Design

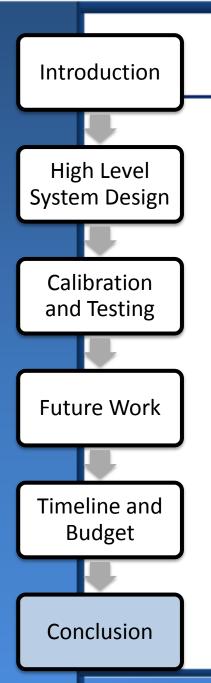
Calibration and Testing

Future Work

Timeline and Budget

Stage		Proposed Date	Actual Date
1	Order Parts	Feb. 7	Apr. 10
	Flow Method Design	Feb. 3	Feb. 10
2	Body Prototyping	Feb. 14	Feb. 10
	Flow Pump Design	Feb. 21	Not Necessary
	Flow Sensing Assembly	Feb. 21	Mar. 7
3	Feedback Loop Integration	Mar. 7	Mar. 15
	Body Assembly	Mar. 7	Mar. 10
	Logging Software Integration	Mar. 21	Apr. 1
4	Building of Test Pump	Mar. 28	Not Necessary
	Formal Testing	Mar. 31	Apr. 15
5	Presentation of Device to Contractors	Apr. 12	Ongoing





Conclusion

 Successful in building of a flow speed probe with response time of 0.5s

 Future work necessary to achieve some of the more difficult requirements set by Kardium

Under budget



Thank You!!!

Questions?

